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PATENT
Docket No. P1296

IN THE
UNITED STATES PATENT AND TRADEMARK OFFICE

APPLICANT(S): BIN YU

SERIAL NO.: 09/826,472

EXAMINER: RON E. POMPEY

FILED: APRIL 4, 2001

ART UNIT: 2812

FOR: METHOD OF FABRICATING A SEMICONDUCTOR DEVICE
HAVING A NITRIDE/HIGH-K/NITRIDE GATE DIELECTRIC
STACK BY ATOMIC LAYER DEPOSITION (ALD) (as herein
amended)

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TRANSMITTAL LETTER

Dear Sir:

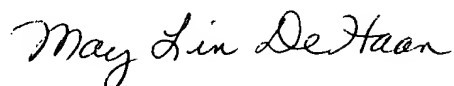
In connection with the above-referenced patent application, transmitted herewith are the following:

1. Response to Office Action (16 pages);
2. Marked-up Version of the Specification (6 pages);
3. Marked-up Version of the Claims (5 pages);

4. Marked-up Version of the Abstract (1 page);
5. Formal Drawings Letter (1 page);
6. Formal Drawings (2 sheets, 6 figures); and
7. Post card in acknowledgment of receipt of all transmitted materials.

Please date stamp and return the enclosed post card to the undersigned in acknowledgment of receipt of all transmitted materials.

Respectfully submitted,



May Lin DeHaan
Reg. No. 42,472

MLD/pa
Date: October 29, 2002
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